

APPROVED	O.G. FIG.
BY	CLASS SUBCLASS
TYMAN	

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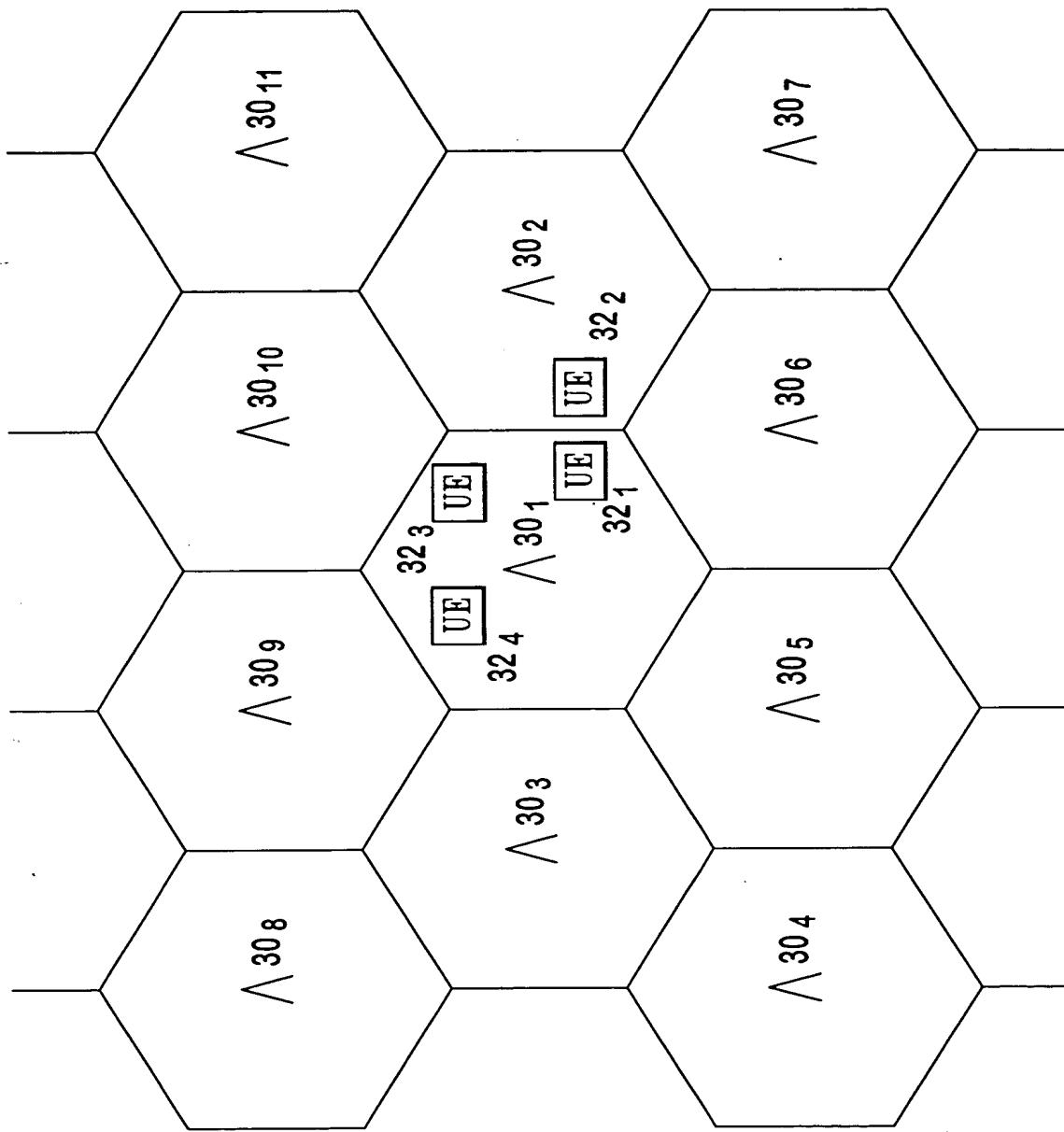
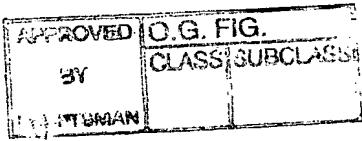


FIG. 1



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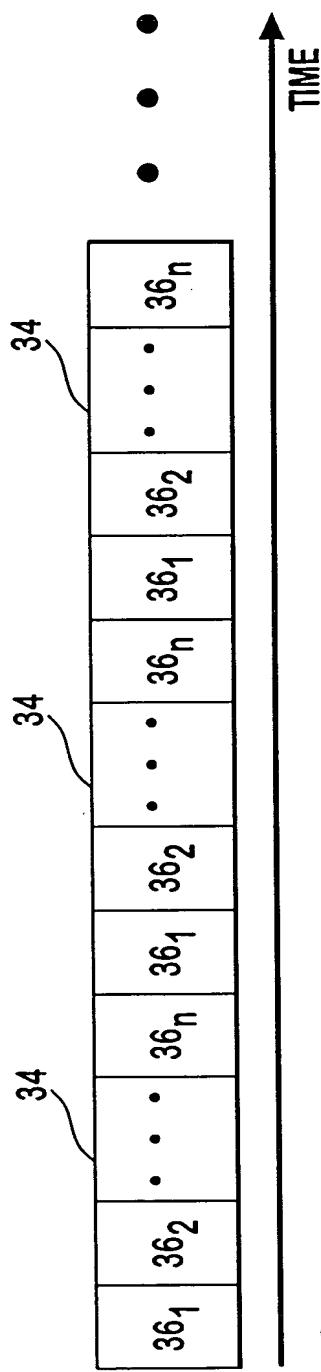


FIG. 2

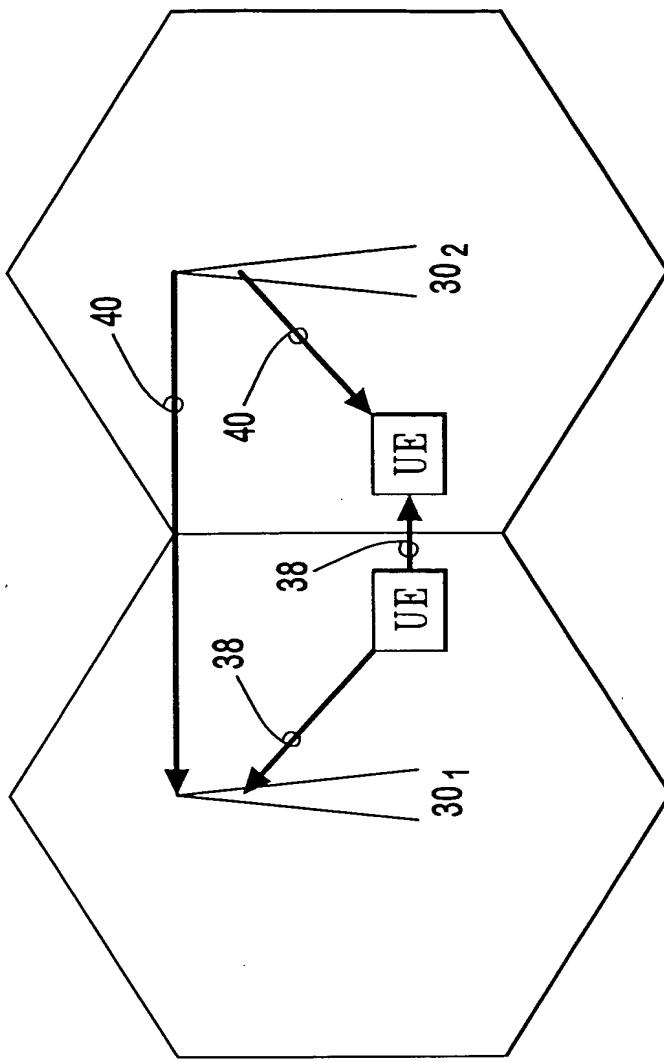


FIG. 3

APPROVED	O.G. FIG.
BY	CLASS SUBCLASS
W. H. STANLEY	

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CELL	POTENTIAL CROSS INTERFERING CELLS										
	1	2	3	4	5	6	7	8	9	10	11
1	X	I	I		I	I*			I	I	
2	I	X			I	I			I	I	
3	I		X	I	I*			I	I		
4			I	X	I						
5	I		I*	I	X	I					
6	I*	I			I	X	I				
7		I			I	X					
8			I					X	I		
9	I		I					I	X	I	
10	I	I							I	X	I
11		I							I	X	

FIG. 6

CELL	TIME SLOT ALLOCATION PER CELL														
	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	S13	S14	S15
1			D	U											
2	D	D							U						
3			U	D											
4										D	D				
5			D										U		
6					D										U
7									D						
8								D	D						
9					D	U									
10			D	U						D	D	D	D		U
11															

FIG. 7

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CELL (UPLINK / DOWNLINK)	TIME SLOT AVAILABILITY														
	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	S13	S14	S15
1(UPLINK)	X	X	X		X	X									
1(DOWNLINK)				X			X		X				X	X	

FIG. 8

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AVAILABILITY LIST				
CELL (UPLINK/DOWNLINK)	TIME SLOT AVAILABILITY			
	S1	S2	...	SN
1(UPLINK)	X	X	...	
1(DOWNLINK)		X	...	X
2(UPLINK)	X	X	...	
2(DOWNLINK)			...	X
.		.	.	
N(UPLINK)	X	X	...	
N(DOWNLINK)		X	...	X

FIG. 4

IDENTIFY BS - BS INTERFERENCE CELLS

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IDENTIFY UE - UE INTERFERENCE CELLS

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DETERMINE WHICH CELLS CROSS
INTERFERE, INCLUDING BOTH
BS - BS AND UE - UE

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FOR EACH CELL, ELIMINATE TIME SLOTS
AVAILABLE FOR THE UPLINK IF A CROSS
INTERFERENCE CELL USES IT FOR THE
DOWNLINK

80

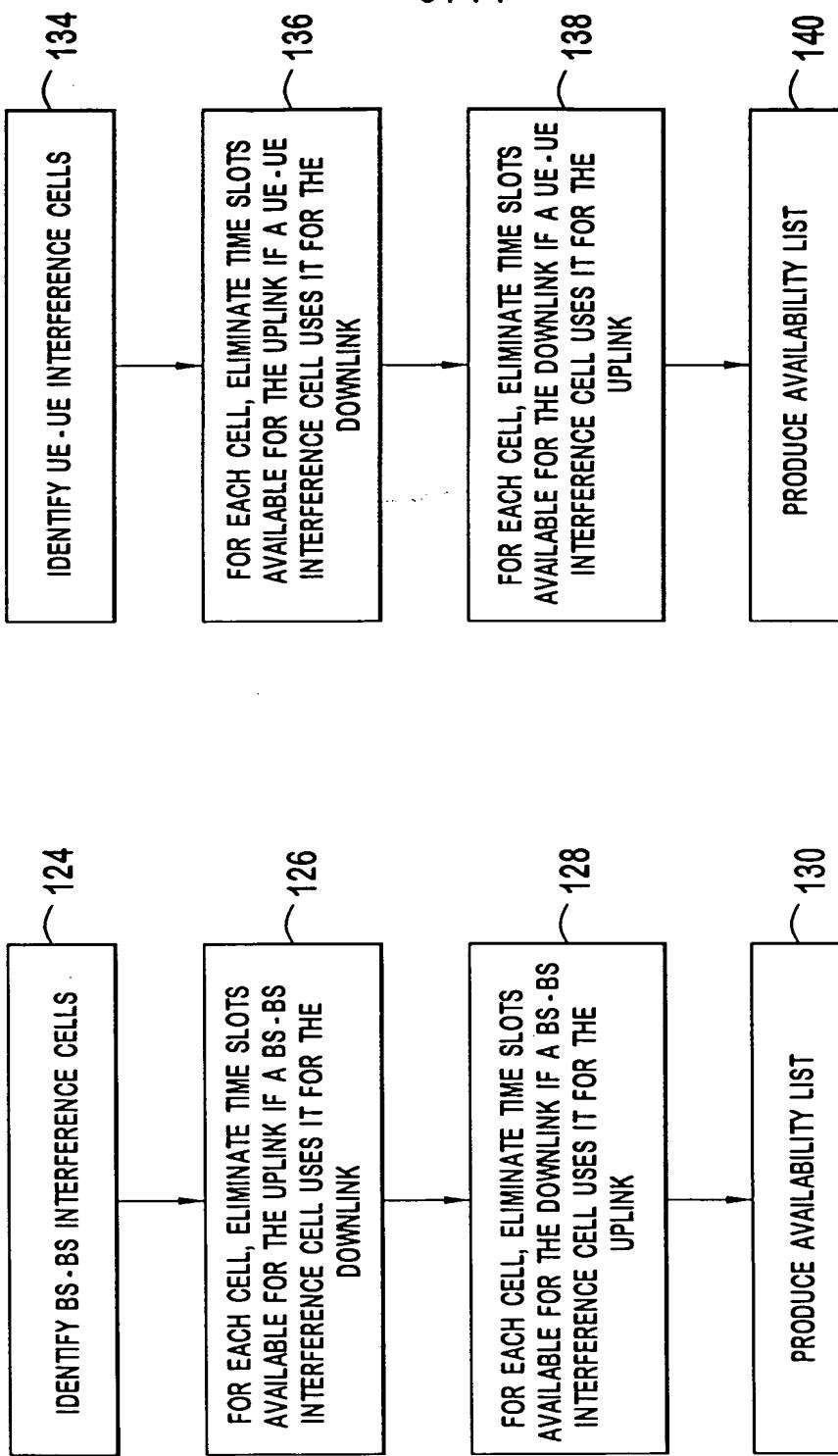
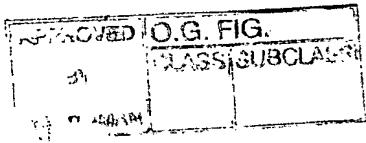
FOR EACH CELL, ELIMINATE TIME SLOTS
AVAILABLE FOR THE DOWNLINK IF A CROSS
INTERFERENCE CELL USES IT FOR THE
UPLINK

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PRODUCE AVAILABILITY LIST

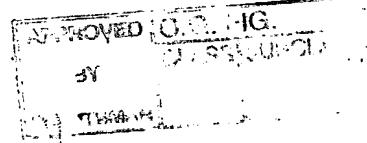
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FIG. 5



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FIG. 11



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CELL	POTENTIAL CROSS INTERFERING CELLS										
	1	2	3	4	5	6	7	8	9	10	11
1	X	I	I		I				I	I	
2	I	X				I	I		I	I	
3	I		X	I				I	I		
4			I	X	I						
5	I			I	X	I					
6		I			I	X	I				
7		I				I	X				
8			I					X	I		
9	I		I					I	X	I	
10	I	I							I	X	I
11		I								I	X

FIG. 10

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CELL	POTENTIAL CROSS INTERFERING CELLS										
	1	2	3	4	5	6	7	8	9	10	11
1	X	I*	I*		I*	I*			I*	I*	
2	I*	X				I*	I*			I*	I*
3	I*		X	I*	I*			I*	I*		
4			I*	X	I*						
5	I*		I*	I*	X	I*					
6	I*	I*			I*	X	I*				
7		I*				I*	X				
8			I*					X	I*		
9	I*		I*					I*	X	I*	
10	I*	I*							I*	X	I*
11		I*								I*	X

FIG. 12

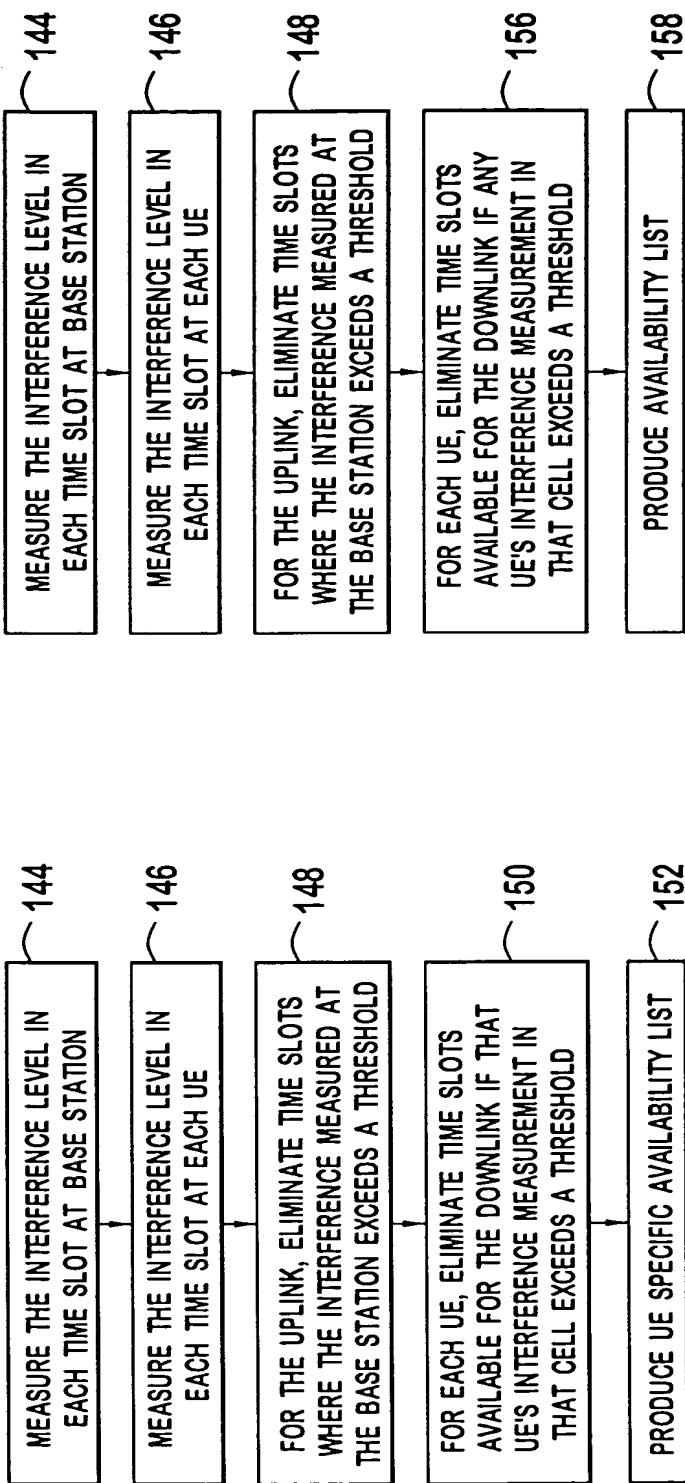
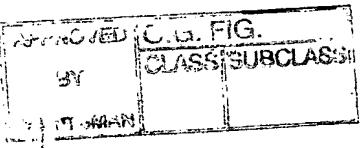
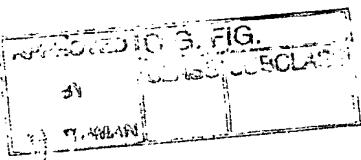


FIG. 13

FIG. 14

AVAILABILITY LIST FOR CELL N															
UE IN CELL N (UPLINK/DOWNLINK)	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	S13	S14	S15
CELL (UPLINK)	X	X				X									
UE 1 (DOWNLINK)			X						X			X			
UE 2 (DOWNLINK)									X			X			
...															
UE N (DOWNLINK)									X	X	X	X			X

FIG. 15



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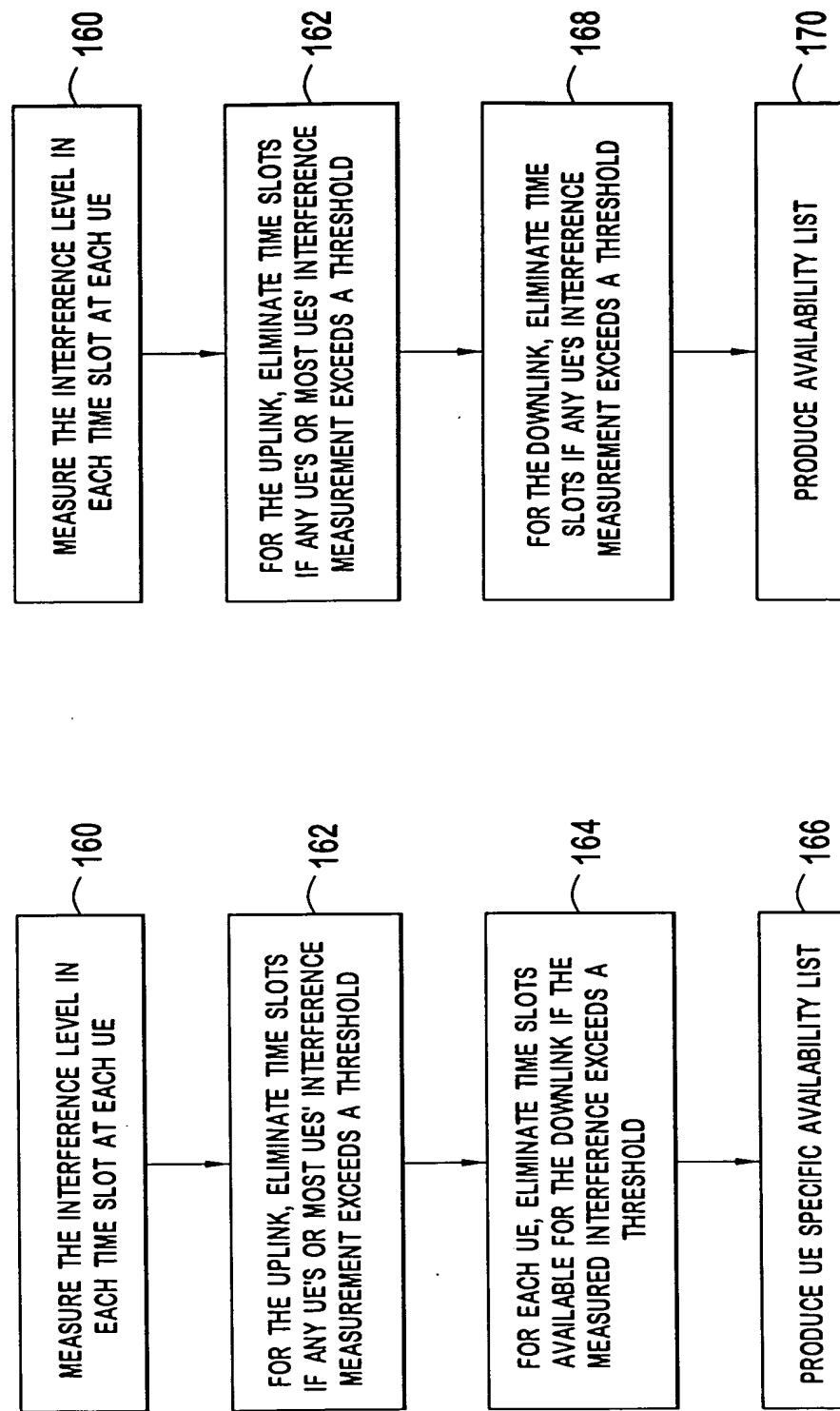


FIG. 16

FIG. 17

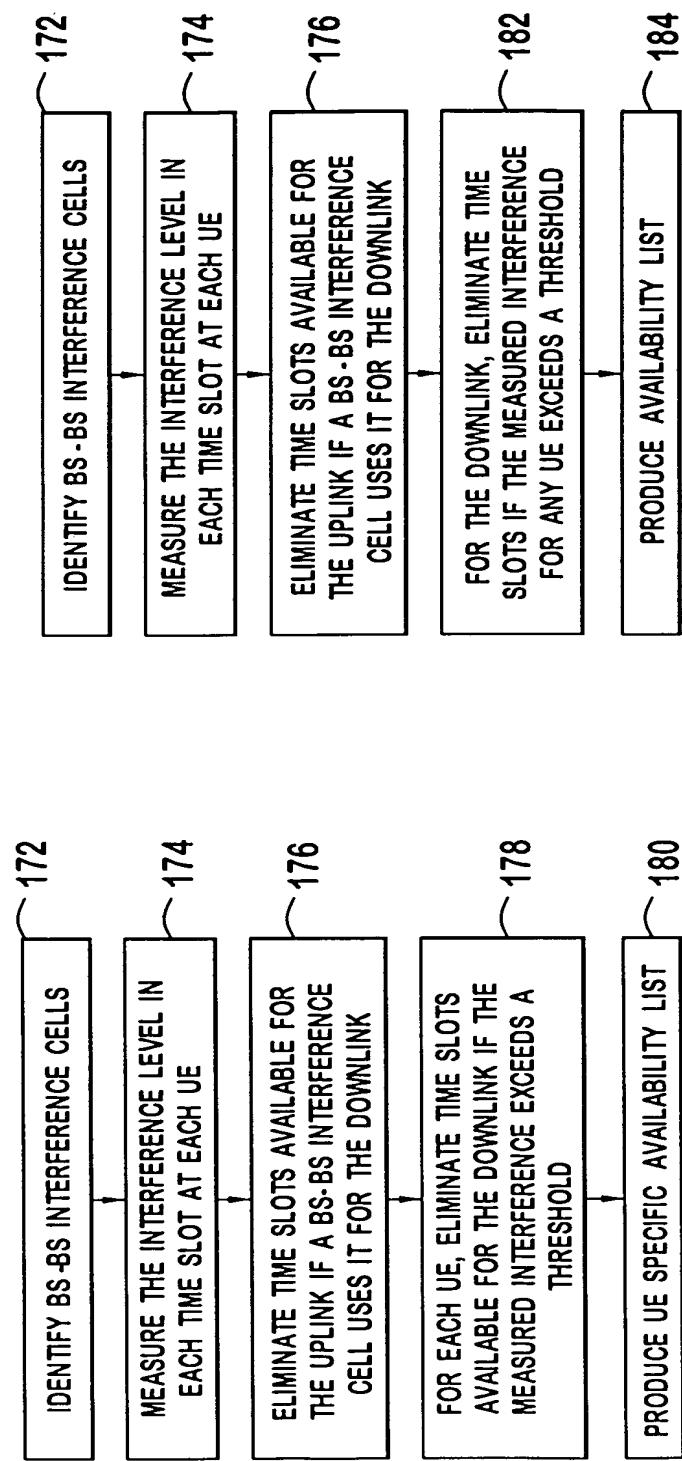
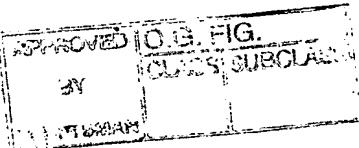
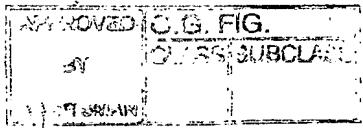


FIG. 18

FIG. 19

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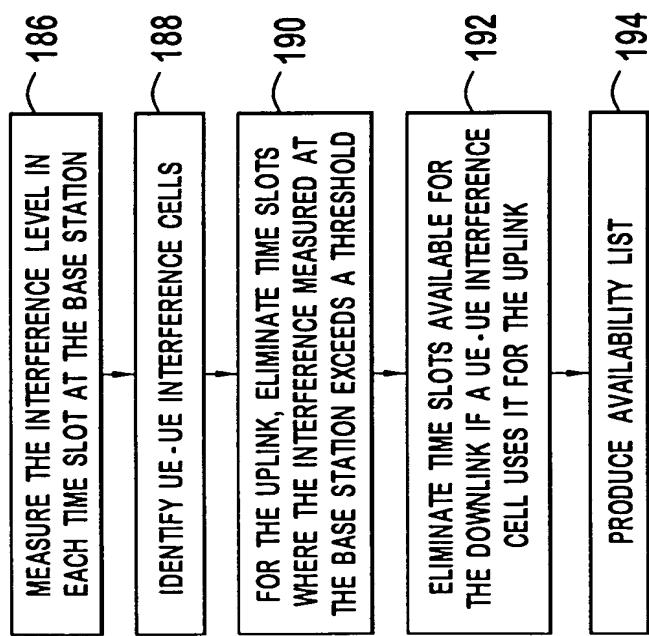
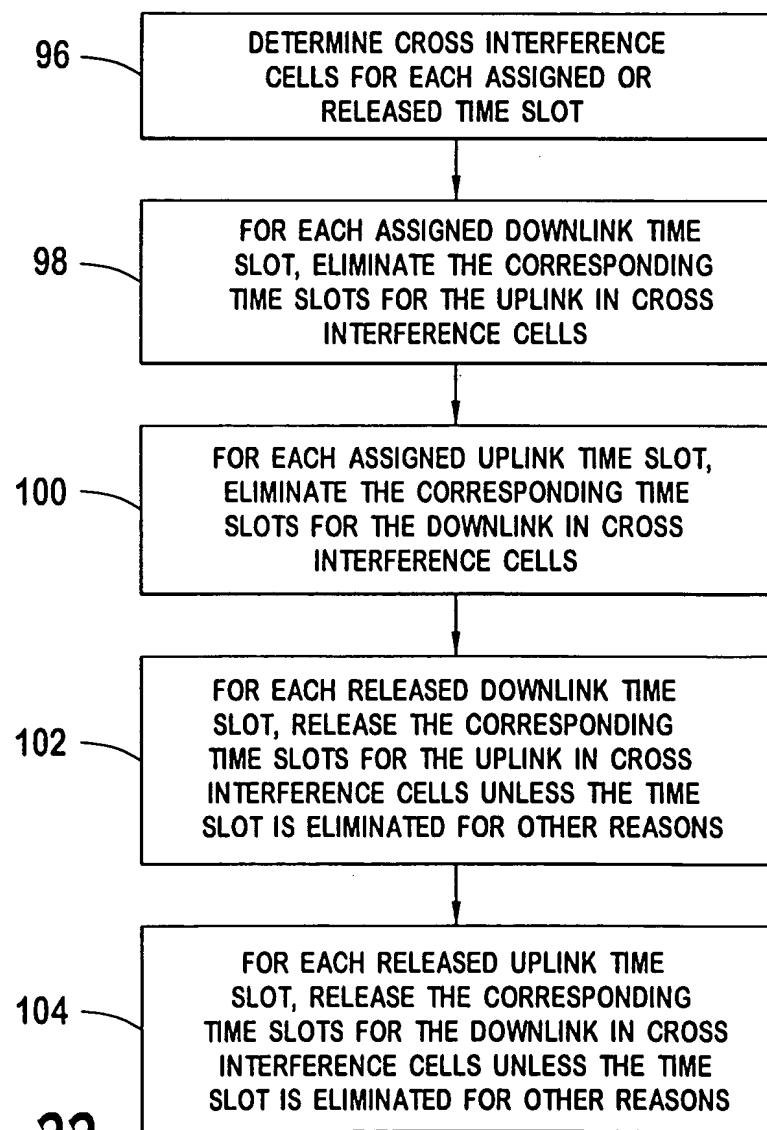
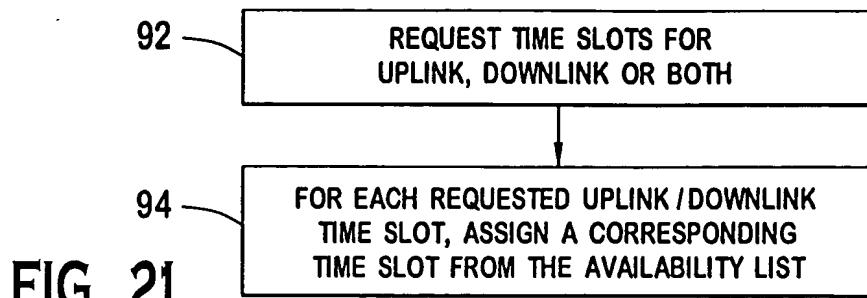
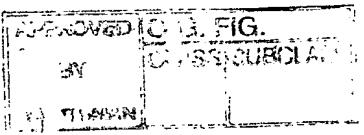


FIG. 20

**FIG. 22**



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CELL	TIME SLOT ALLOCATION														
	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	S13	S14	S15
1			D	U											
2	D	D							U						
3			U	D											
4										D	D				
5		D											U		
6					D**	D*	U*							U	
7										D					
8										D	D				
9					D	U									
10		D	U							D	D	D	D		U
11															

FIG. 23

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CELL (UPLINK / DOWNLINK)	TIME SLOT AVAILABILITY														
	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	S13	S14	S15
7 (UPLINK)	X	X				R	X*								
7 (DOWNLINK)								X*	X						X

FIG. 24

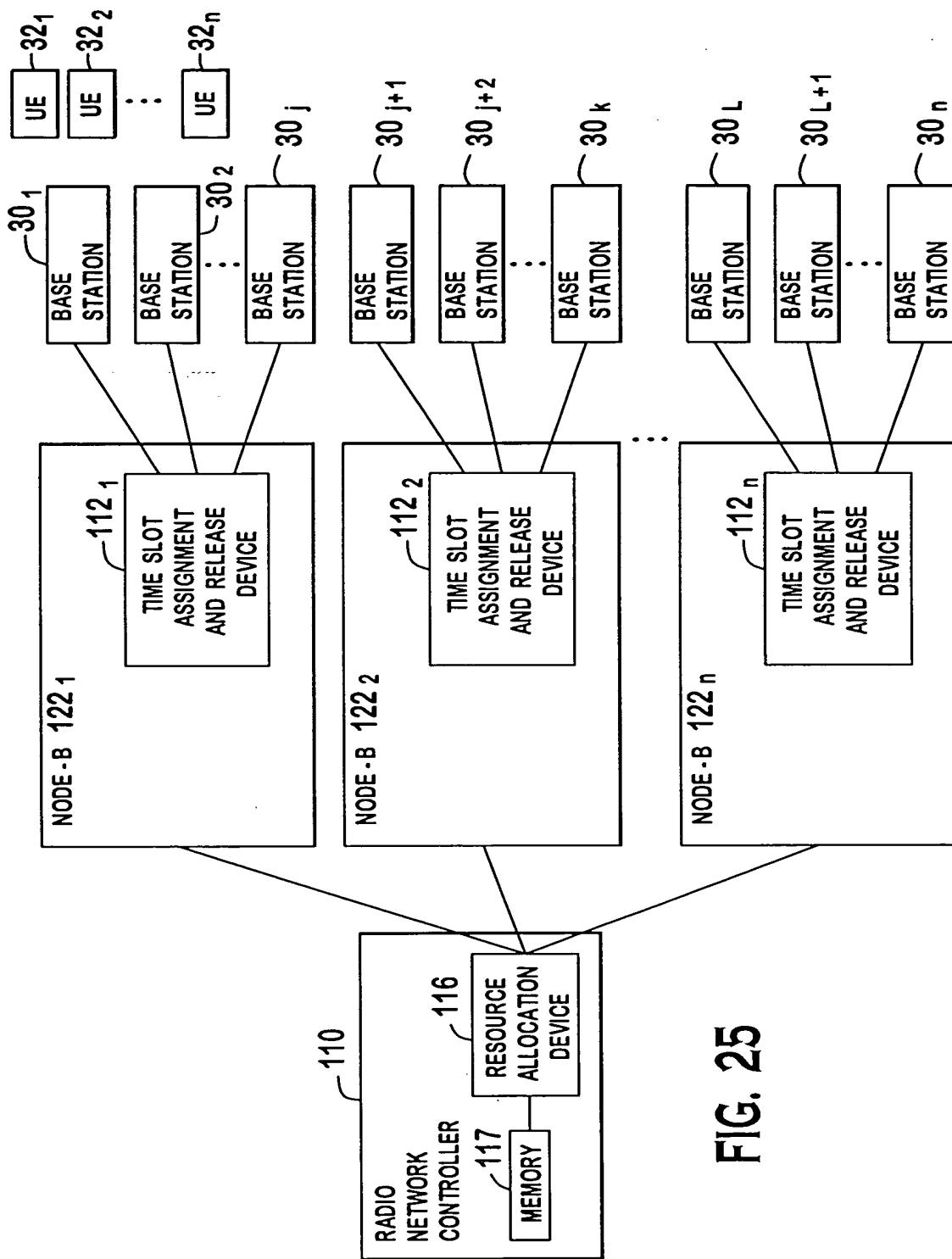


FIG. 25

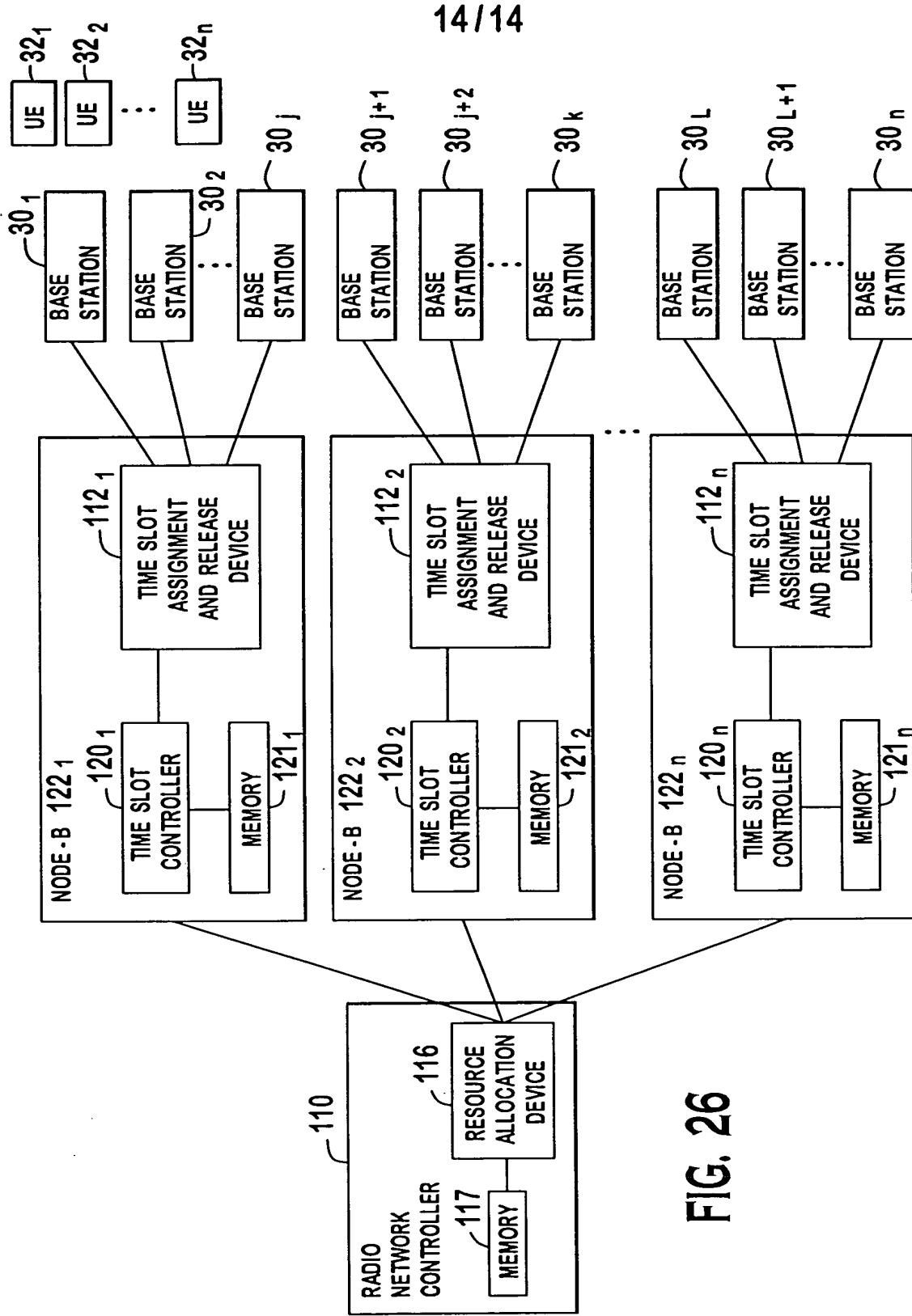
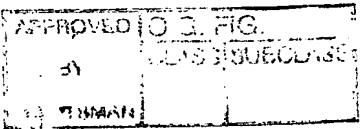


FIG. 26